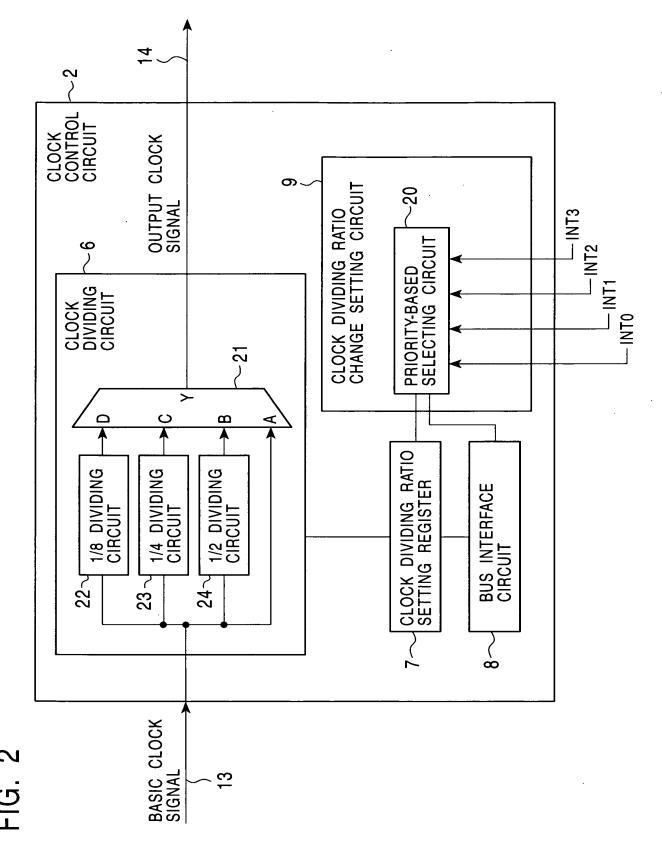


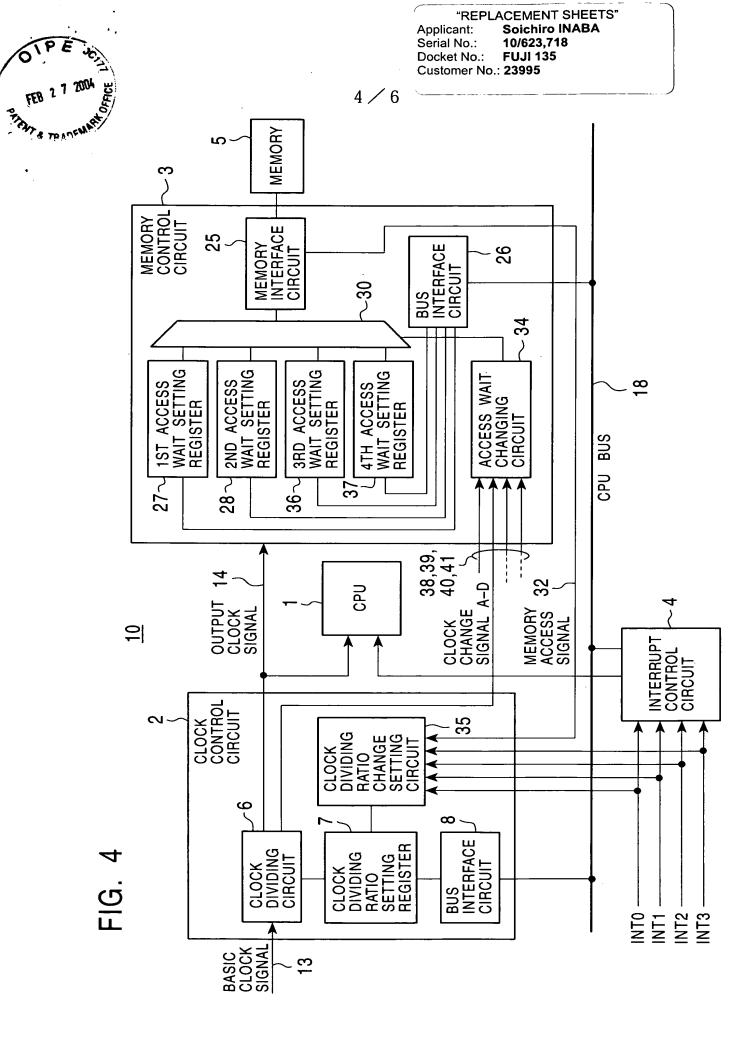
Applicant: Serial No.: Docket No.:

Customer No.: 23995

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"REPLACEMENT SHEETS" ant: Soichiro INABA Applicant: Serial No.: Docket No.: 10/623,718 **FUJI 135** FEB 2 7 2004 5 Customer No.: 23995 3/6MEMORY က MEMORY CONTROL CIRCUIT MEMORY INTERFACE CIRCUIT 25 BUS INTERFACE CIRCUIT 26 **6**30 29 1ST ACCESS WAIT SETTING . REGISTER WAIT SETTING REGISTER ACCESS WAIT CHANGING CIRCUIT 8 2ND ACCESS 28 BUS CPU CLOCK CHANGE REQUEST SIGNAL 4 CPU 위 OUTPUT CLOCK SIGNAL MEMORY ACCESS SIGNAL ب INTERRUPT CONTROL CIRCUIT CLOCK CONTROL CIRCUIT 33 CLOCK DIVIDING RATIO CHANGE SETTING CIRCUIT 9 ∞ BUS INTERFACE CIRCUIT CLOCK DIVIDING RATIO SETTING REGISTER CLOCK DIVIDING CIRCUIT INT0 N T BASIC CLOCK SIGNAL 13



"REPLACEMENT SHEETS" ant: Soichiro INABA Applicant: Serial No.:

10/623,718 Docket No.: **FUJI 135** Customer No.: 23995



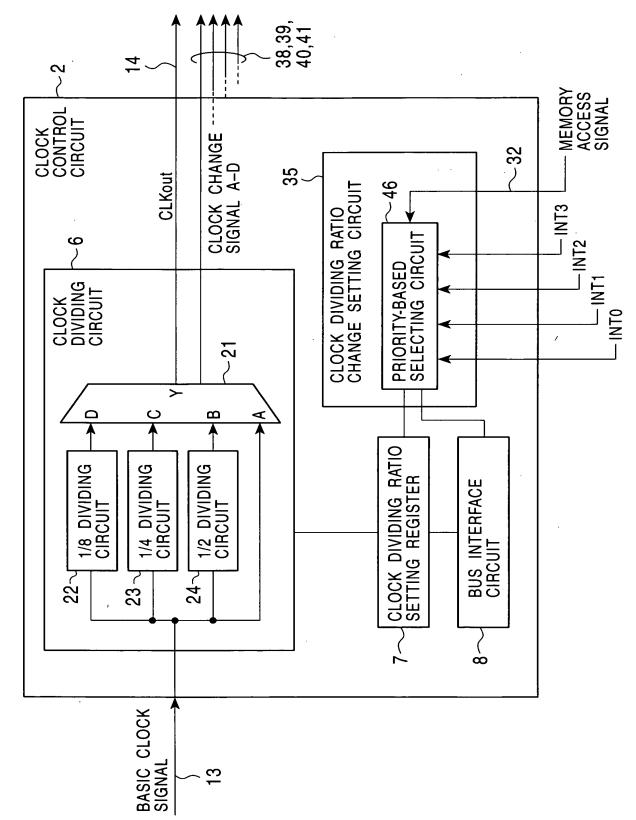


FIG. 5

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